

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Letters Patent of:
Toshiaki Kurachi et al.

Patent No.: 7,088,056

Issued: August 8, 2006

For: Bulb Type Electrodeless Fluorescent Lamp

**REQUEST FOR CERTIFICATE OF CORRECTION
PURSUANT TO 37 CFR 1.322**

Attention: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Upon reviewing the above-identified patent, Patentee noted typographical errors which should be corrected.

In the Claims:

Column	Line	Claim	Description
12	21	1	Before "protrusion", insert --a--
12	27	2	Delete "part" and insert --an upper end--
12	28	2	After "bulb", delete remainder of claim and insert --in such a manner as to lift the luminous bulb in a direction opposite to the base, thereby allowing the connection wire to be disposed spaced apart from the sealing portion.--
12	38	4	After "wherein the", delete remainder of claim and insert --connection wire and the sealing portion are spaced apart from each other by 0.3 mm or more.--

12	41	5	After "wherein", delete remainder of claim and insert -- the greatest length of the circuit board is 60 mm or less.-
12	43	6	After "lamp", delete remainder of claim and insert --of claim 1, wherein a phosphor or a protective coating is not applied to an inner wall of the sealing portion.--
13	41	8	After "circuit", insert --board--
14	34	9	Delete "claim 6" and insert --claim 8--
14	37	10	Delete "claim 6" and insert --claim 8--
14	40	11	Delete "claim 6" and insert --claim 8--

The errors were not in the application as filed by applicant; accordingly no fee is required.

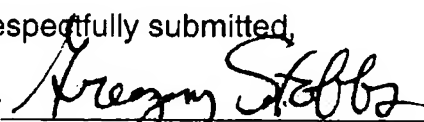
Transmitted herewith is a proposed Certificate of Correction effecting such amendment. Patentee respectfully solicits the granting of the requested Certificate of Correction.

Applicant believes no fee is due with this request. However, if a fee is due, please charge our Deposit Account No. 08-0750, under Order No. 5077-000219/US/NP from which the undersigned is authorized to draw.

Dated: October 27, 2006

Respectfully submitted,

By



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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO. : 7,088,056
APPLICATION NO. : 10/506,445
ISSUE DATE : August 8, 2006
INVENTOR(S) : Toshiaki Kurachi et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column Line	Claim	Description
12	21 1	Before "protrusion", insert --a--
12	27 2	Delete "part" and insert --an upper end--
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12	38 4	After "wherein the", delete remainder of claim and insert --connection wire and the sealing portion are spaced apart from each other by 0.3 mm or more.--
12	41 5	After "wherein", delete remainder of claim and insert --the greatest length of the circuit board is 60 mm or less.--
12	43 6	After "lamp", delete remainder of claim and insert --of claim 1, wherein a phosphor or a protective coating is not applied to an inner wall of the sealing portion.--
13	41 8	After "circuit", insert --board--
14	34 9	Delete "claim 6" and insert --claim 8--
14	37 10	Delete "claim 6" and insert --claim 8--
14	40 11	Delete "claim 6" and insert --claim 8 --

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